

#### 3W isolated DC-DC converter in SIP package Wide input and regulated single output



## **FEATURES**

- Wide 2:1 input voltage range
- I/O isolation test voltage 1500VDC
- Operating ambient temperature range: -40°C to **+85**℃
- No-load power consumption as low as 0.05W
- Output short-circuit protection

VRB0505S-3WR3G of isolated 3W DC-DC converter products with a wide 2:1 input voltage range, low no-load power consumption. 1500VDC input to output isolation, operating ambient temperature range of -40  $\degree$  to +85  $\degree$ , output short-circuit protection and they are widely used in applications such as electric power, and instruments fields.

Selection	Guide						
		Input Volt	age (VDC)	Ou	tput	Full Load	Capacitive
Certification	Part No.	Nominal (Range)	Max.®	Voltage(VDC)	Current (mA) Max./Min.	Efficiency <sup>®</sup> (%) Min./Typ.	Load (µF)Max.
	VRB0505S-3WR3G	5 (4.5-9)	11	5	500/0	71/73	2200
Notes:							

① Exceeding the maximum input voltage may cause permanent damage;

2 Efficiency is measured at nominal input voltage and rated output load.

Input Specifications					
Item	Operating Conditions	Min.	Typ.	Max.	Unit
Input Current (full load / no-load)	nominal input voltage		761/10	782/16	mA
Reflected Ripple Current	nominal input voltage	-	20		
Surge Voltage (1sec. max.)		-0.7		12	1/20
Start-up Voltage		-		4.5	VDC
Input Filter			Capacita	ince Filter	
Hot Plug			Unava	ailable	

<b>Output Specification</b>	S				
Item	Operating Conditions	Min.	Тур.	Max.	Unit
	5% -100% load		±l	±3	
Voltage Accuracy	0% -5% load			±5	
Linear Regulation	Input voltage variation from low to high at full load		±0.2	±0.5	~ %
Load Regulation <sup>®</sup>	5% -100% load		±0.6	±l	
Transient Recovery Time	25% load step change, nominal input voltag		0.5	3	ms
Transient Response Deviation	25% load step change, input voltage range			±5	%
Temperature Coefficient	Full load			±0.03	<b>%/</b> ℃
Ripple & Noise <sup>®</sup>	20MHz bandwidth, 5% -100% load		40	75	mVp-p
Short-circuit Protection	Input voltage range		Continuous,	self-recovery	/
Note:					

①Load regulation for 0% -100% load increases to ±5%;

2) The "parallel cable" method is used for ripple and noise test, please refer to DC-DC Converter Application Notes for specific information.



## DC/DC Converter VRB0505S-3WR3G

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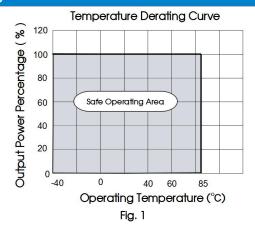
<b>General Specificat</b>	ion				
Item	Operating Conditions	Min.	Тур.	Max.	Unit
Isolation	Input-output Electric Strength test for 1 minute with a leakage current of 1mA max.	1500			VDC
Insulation Resistance	Input-output insulation at 500VDC	1000			MΩ
Isolation Capacitance	Input-output capacitance at 100kHz/0.1V		1000		pF
Operating Temperature	See Fig. 1	-40		+85	
Storage Temperature		-55		+125	°C
Pin Soldering Resistance Temperature	Soldering spot is 1.5mm away from case for 10 seconds			+300	
Storage Humidity	Without condensation	5		95	%RH
Switching Frequency	PWM mode		300		kHz
MTBF	MIL-HDBK-217F@25°C	1000			k hours

Note: ①Switching frequency is measured at full load. The module reduces the switching frequency for light load (below 50%) efficiency improvement.

Mechanical Specifico	cal Specifications		
Case Material	Black plastic; flame-retardant and heat-resistant (UL94-V0)		
Dimensions	22.00 x 9.50 x 12.00 mm		
Weight	4.5g (Typ.)		
Cooling method	Free air convection		

Electromo	agnetic Con	npatibility (EM	C)	
Emissions	CE	CISPR32/EN55032	CLASS B (see Fig.3- $\ensuremath{\textcircled{2}}$ for recommended circuit)	
ETTISSIONS	RE	CISPR32/EN55032	CLASS B (see Fig.3- $\ensuremath{\textcircled{2}}$ for recommended circuit)	
	ESD	IEC/EN61000-4-2	Contact ±4kV	perf. Criteria B
	RS	IEC/EN61000-4-3	10V/m	perf. Criteria B
Immunity	EFT	IEC/EN61000-4-4	$\pm 2kV$ (see Fig.3-(1) for recommended circuit)	perf. Criteria B
	Surge	IEC/EN61000-4-5	line to line ±2kV (see Fig.3- $(1)$ for recommended circuit)	perf. Criteria B
	CS	IEC/EN61000-4-6	3 Vr.m.s	perf. Criteria B

## Typical Characteristic Curves



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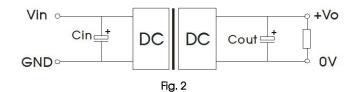


## Design Reference

#### 1. Typical application

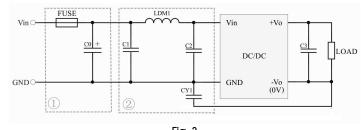
All the DC/DC converters of this series are tested before delivery using the recommended circuit shown in Fig. 2.

Input and/or output ripple can be further reduced by appropriately increasing the input & output capacitor values Cin and Cout and/or by selecting capacitors with a low ESR (equivalent series resistance). Also make sure that the capacitance is not exceeding the specified max. capacitive load value of the product.



Vout (VDC)	Cin	Cout
5	100µF/50V	100µF/25V

#### 2. EMC compliance circuit



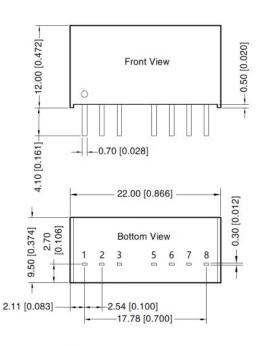
Components	Vin: 5VDC
FUSE	Slow-blow, selecting based on needs
C0	680µF/25V
C1	4.7µF/50∨
LDM1	12µH
C2	4.7µF/50∨
C3	100µF/50V
CY1	1nF/2kV

Fig. 3 Notes: For EMC tests we use Part in Fig. 3 for immunity and part for emissions test. Selecting based on needs

#### 3. The products do not support parallel connection of their output

4. For additional information please refer to DC-DC converter application notes on <u>www.mornsun-power.com</u>

#### Dimensions and Recommended Layout



Note: Unit: mm[inch] Pin section tolerances:  $\pm 0.10[\pm 0.004]$ General tolerances:  $\pm 0.50[\pm 0.020]$ 

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¢1.00 [¢0.039]

THIRD ANGLE PROJECTION



Pin	-Out
Pin	Mark
1	GND
2	Vin
3	NC
5	NC
6	+Vo
7	OV
8	NC

NC: Not available for electrical connection

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#### Note:

- 1. For additional information on Product Packaging please refer to <u>www.mornsun-power.com</u>, packaging number: 58210004;
- 2. The maximum capacitive load offered were tested at input voltage range and full load;
- 3. Unless otherwise specified, parameters in this datasheet were measured under the conditions of Ta=25°C, humidity<75%RH with nominal input voltage and rated output load;
- 4. All index testing methods in this datasheet are based on company corporate standards;
- 5. We can provide product customization service, please contact our technicians directly for specific information;
- 6. Products are related to laws and regulations: see "Features" and "EMC";
- 7. Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by gualified units.

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